

Fig. 1

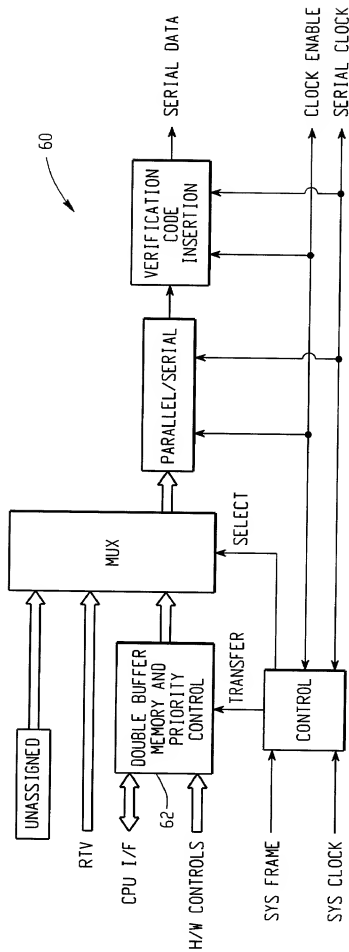
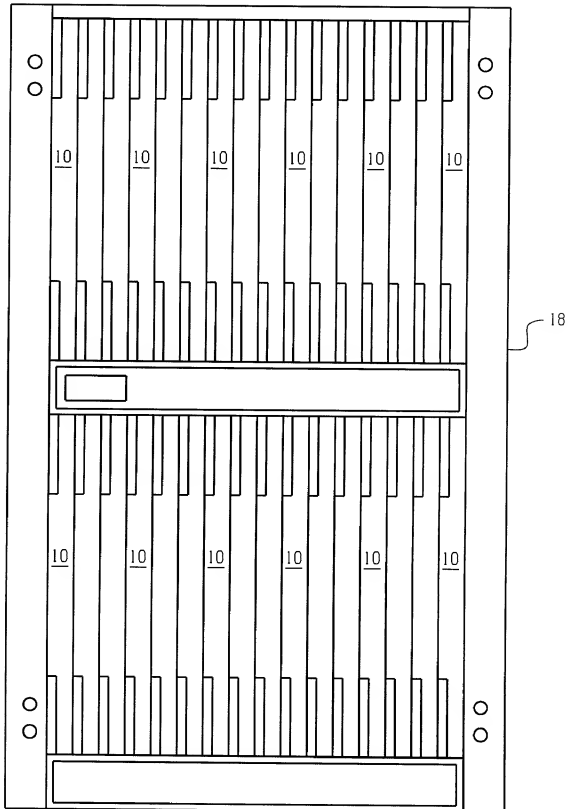
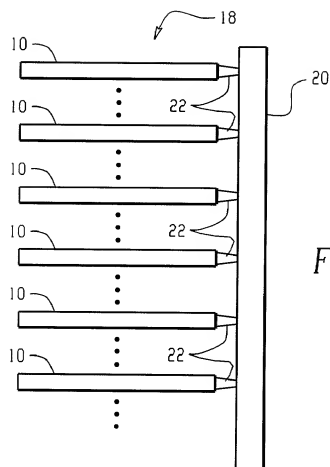
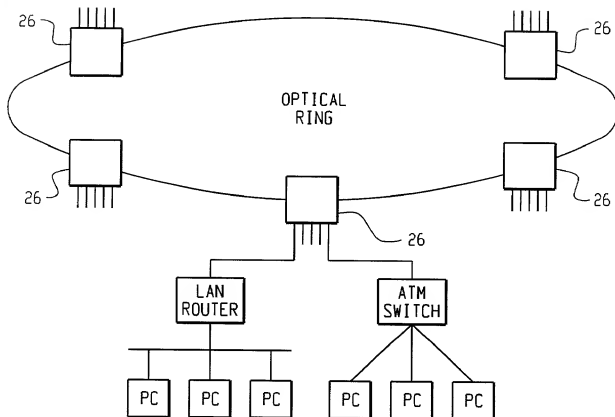


Fig. 10

2

*Fig. 2*

*Fig. 3**Fig. 4*

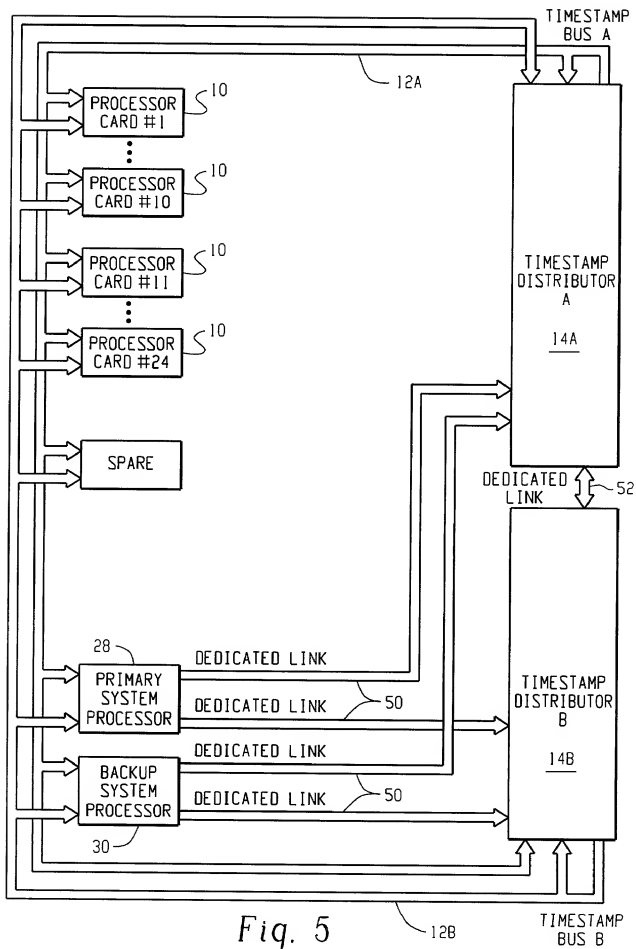


Fig. 5

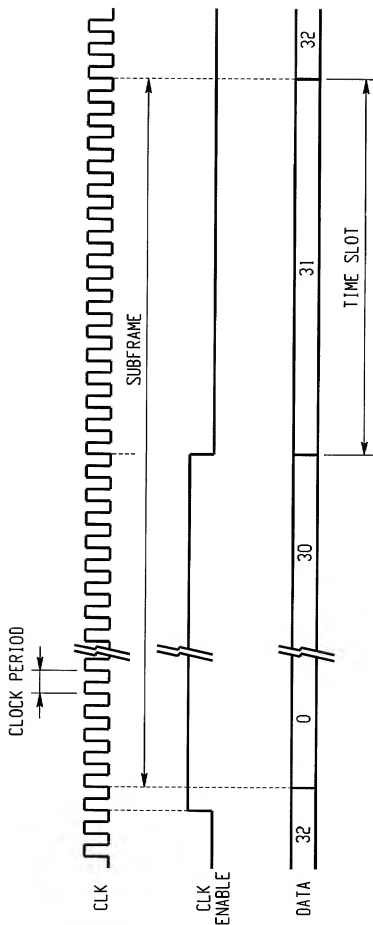


Fig. 6

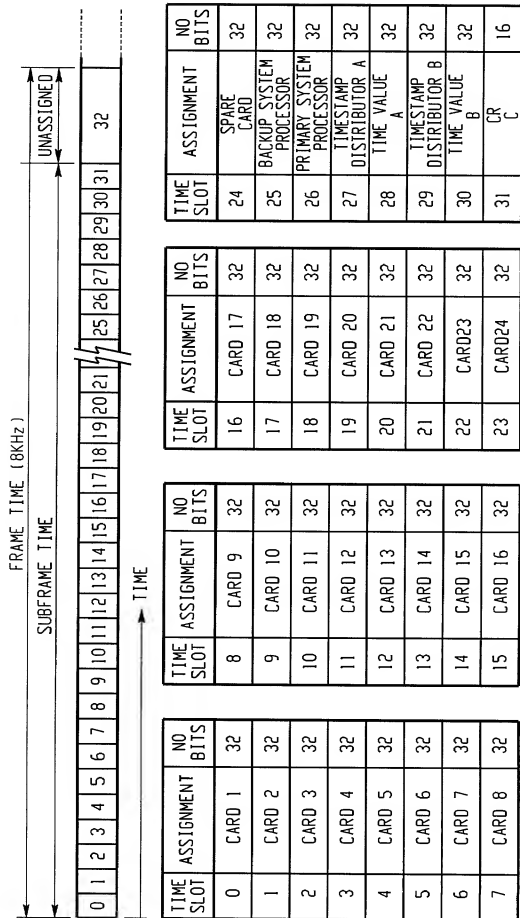


Fig. 7

TIME SLOT	ASSIGNMENT	NO BITS
32	UNASSIGNED	207

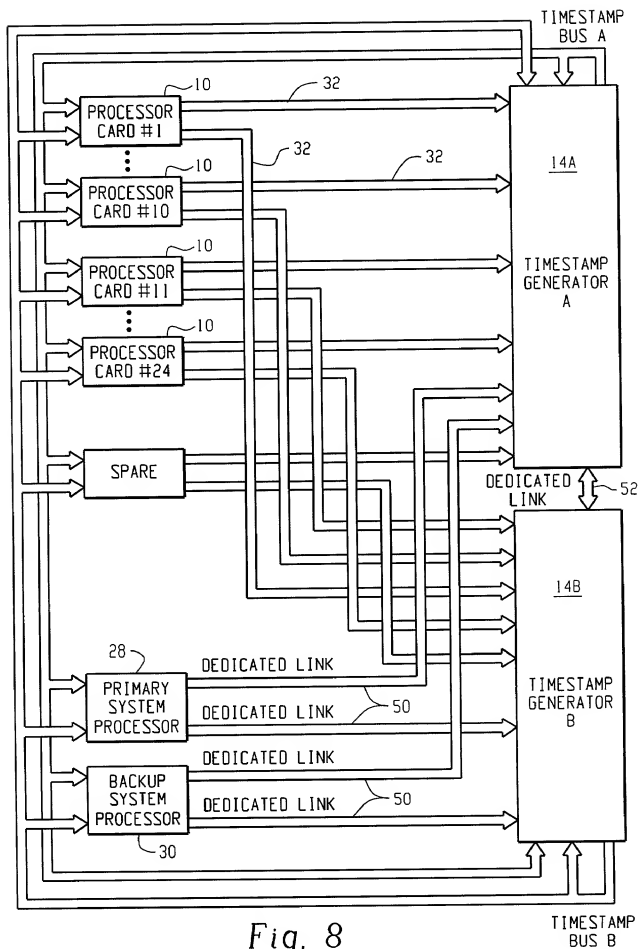


Fig. 8

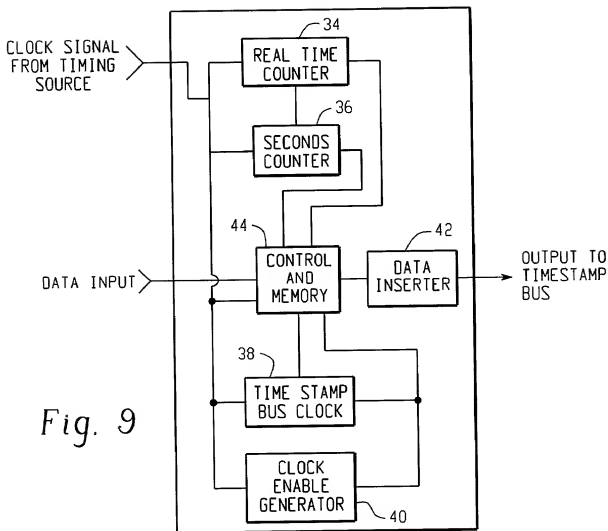


Fig. 9

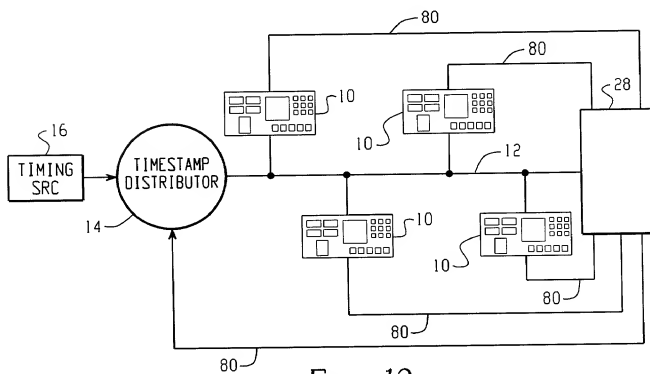


Fig. 12

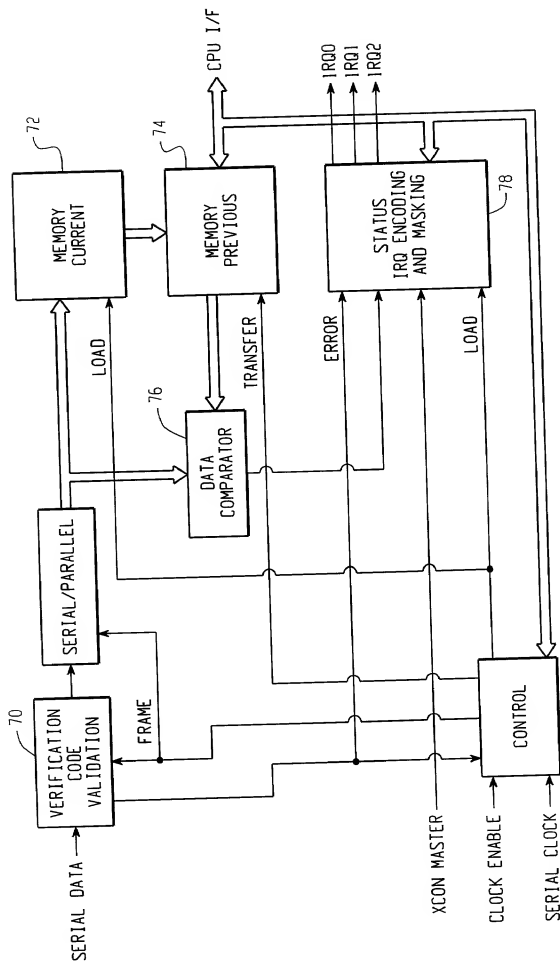


Fig. 11